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ANALOG ELECTRONICS VIVA & INTERVIEW QUESTIONS PROF. HITESH DHOLAKIYA for computing mathematical functions such as addition, subtraction ,multiplication, integration & differentiation. Q-35 List out the ideal characteristics of OPAMP? Ans (i) Open loop gain infinite (ii)Input impedance infinite (iii)Output impedance low (iv)Bandwidth infinite

Analog Electronics viva & interview questions

Analog Communication VIVA Questions :- Analog Communication is a data transmitting technique in which information signal is transmitted in analog nature. This always utilizes continuous signals to transmit data which may obtained from audio, image, video etc. An Analog signal is a variable signal continuous in both time and amplitude.

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Analog Electronic Circuits Laboratory Viva Questions 6. 1. If the emitter bias current increased in a differential amplifier then. 1. Input impedance increases 2. CMRR increases 3. The gain of the circuit will increase. 2. Which of the following is not the characteristic of the first stage of the op-amp.

Analog Electronic Circuits Laboratory Viva Questions 6 ...

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VIVA QUESTION FOR ANALOG COMMUNICATION: 1. What is amplitude modulation? 2. What is modulation? 3. What are the different types of analog modulation? 4. What is the need for modulation? 5. What are the objectives met by modulation? 6. What are the advantages of PWM over PAM? 7. What is pulse position modulation? 8. What is the advantage of PPM over PWM and PAM? 9.

VIVA QUESTION FOR ANALOG COMMUNICATION

ANALOG ELECTRONICS Questions :-1. The early effect in a bipolar junction transistor is caused by A. fast turn-on B.fast turn-off C. large collector-base reverse bias D. large emitter-base forward bias. 2. MOSFET can be used as a A. current controlled capacitor B. voltage controlled capacitor C. current controlled inductor D. voltage controlled inductors. 3.

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Electronic Circuit Analysis Lab Viva Questions with Answers Part I. 1. What is difference between Amplifier and Attenuator? Both are linear systems but Amplifier's gain is more than unity (+ve dB), Attenuator gain is less than unity (-ve dB) 2.Which Amplifier will amplify voltage and current? common emitter amplifier.

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Analog and Digital Communication Viva Questions ...

Digital Electronics Interview Questions. A list of top frequently asked Digital Electronics Interview Questions and answers are given below. 1) What is the difference between Latch And Flip-flop? The difference between latches and Flip-flop is that the latches are level triggered and flip-flops are edge triggered.

Top 39 Digital Electronics Interview Questions - javatpoint

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Practical Electronics Handbook

The process took 2 weeks. I interviewed at Analog Devices. Interview. Went to campus recruiting event and talk to the staff member on site. After few days, receive an email to schedule a phone interview. On the phone interview, staff was friendly and ask questions basically just on the resume and ask me whether I have any questions or not.

Analog Devices Product Engineer Interview Questions ...

Finally, a group of appropriate questions are presented. For those with longer scheduled lab times, a ... an electronics laboratory. The primary purpose of the oscilloscope is to plot a voltage versus time ... While the modern digital oscilloscope on the surface appears much like its analog ancestors, the internal

Laboratory Manual for AC Electrical Circuits

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Analog Communication Tutorial Power Electronics Interview Questions ; Question 8. Give Two Ways Of Converting A Two Input Nand Gate To An Inverter? Answer : a) Short the 2 inputs of the nand gate and apply the single input to it. b) Connect the output to one of the input and the other to the input signal. Question 9. Why Is Most Interrupts Active Low?

TOP 250+ Digital Electronics Interview Questions and ...

ANALOG ELECTRONICS LABORATORY LAB MANUAL ... Viva questions 47-48 7. Appendix-1 49-52 . Analog Electronics Laboratory Manual - 10ESL37 Dept of ECE- CCEM Page ii SYLLABUS 1. Design and set up the following rectifiers with and without filters and to determine ripple factor and rectifier efficiency: